

XC6SLX25-2FTG256C Xilinx

As the professional supplier, we would like to provide you high quality XT-ShenZhen® XC6SLX25-2FTG256C Xilinx. And we will offer you the best after-sale service and timely delivery. XC6SLX25-2FTG256C Xilinx is a high-performance, programmable logic chip designed and developed by Xilinx Corporation. It is one of the Xilinx Spartan-6® FPGA series chips. Manufactured with 45nm technology, it is ideal for mid-level bridging applications in automotive infotainment, and consumer and industrial automation. There are procurement needs for high-end chips in various fields, please contact XT-ShenZhen® too!!!

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Product description

The XC6SLX25-2FTG256C Xilinx Spartan-6® FPGA series provides industry-leading system integration for high-volume applications. The Spartan-6® FPGA series contains two domain optimization sub-series, LX (Logic Optimized), and LXT (High-Speed Serial Connectivity). The Spartan-6® LX FPGA is suitable for low cost and provides up to eight low-power 3.2Gb/s serial transceivers to improve system performance. The series supports up to 147K logic cell density, 4.8Mb of memory, integrated memory controller, DSP48A1 chip, and high-performance industry-standard integrated IP. The Spartan-6® LXT FPGA is suitable for serial connectivity and provides the industry's high reliability, low risk, low-cost solution.

Product features

- . Advanced Power Management, SelectIO™ Interface Technology
- . 800Mb/s DDR3, integrated memory controller, zero power consumption, sleep save mode
- . BoM cost reduction, Clock Management Module (CMT) for improved performance
- . Automatic detection of configuration options and enhanced IP security with AES and device DNA protection
- . ISE® design kit
- Multi-port bus architecture with independent FIFOs reduces design timing issues
- Rich logic resources and increased logic capacity

Shenzhen Zhen Rong Era Supply Chain Management Co., Ltd.

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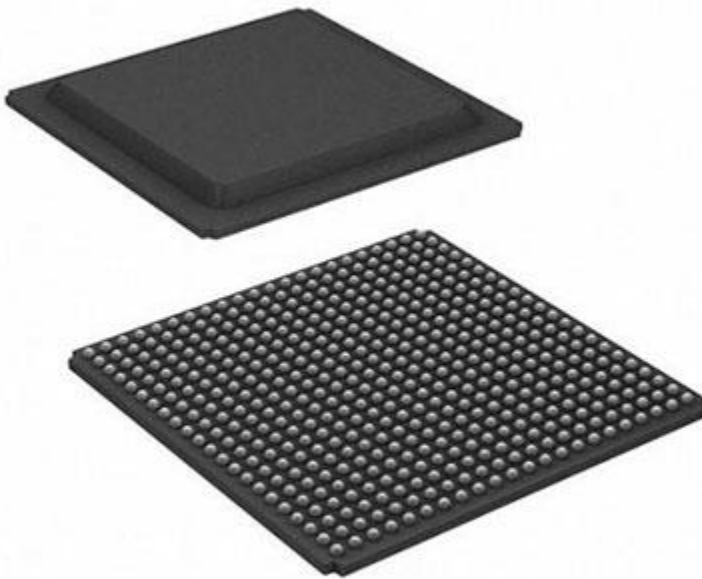
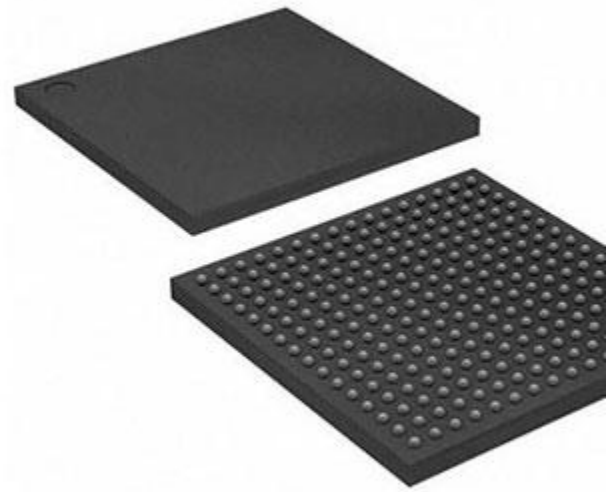
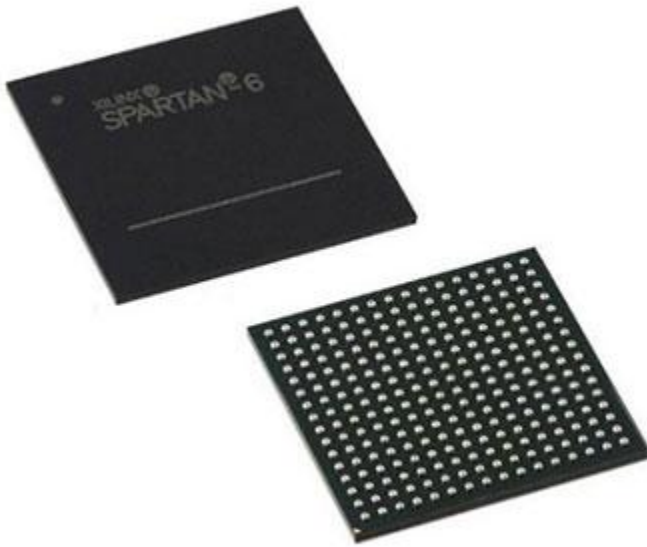
Phone: +86-13927462033

- Frequency synthesis with simultaneous multiplication, division, and phase shift • 16 low-skew global clock networks
- Efficient 6-input LUTs improve performance and minimize power consumption
- 3.3V to 1.2V I/O standards and protocols • Low-cost HSTL and SSTL memory interfaces
- Up to 1,080 Mb/s data transfer rate per differential I/O
- Supports remote upgrades via multiple bitstreams, protected with watchdogs

Product parameters

Technical parameters	The number of pins	256
	RAM size	119808B
	Number of inputs/outputs	186 Input
	Operating Temperature (Max)	85°C
	Operating Temperature (Min)	0°C
	Supply voltage	1,14V-1.26V
	encapsulation	BGA -256
	Packaging	Each
	RoHS standard	RoHS Compliant
	Leaded standard	Lead-Free

Product images



FAQ

Q: Are you a trader or a manufacturer?

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A: Yes, we are traders. XC6SLX25-2FTG256C Xilinx we sell are purchased directly from the factory.

Q: What is your MOQ?

A: We will decide according to the product packaging method, and will communicate with you in time before placing an order.

Q: Are your shipments fast?

A: Yes, we have our own warehouse, and most of the goods are in stock. Same-day delivery can be achieved as soon as possible.